

FIGURE 1

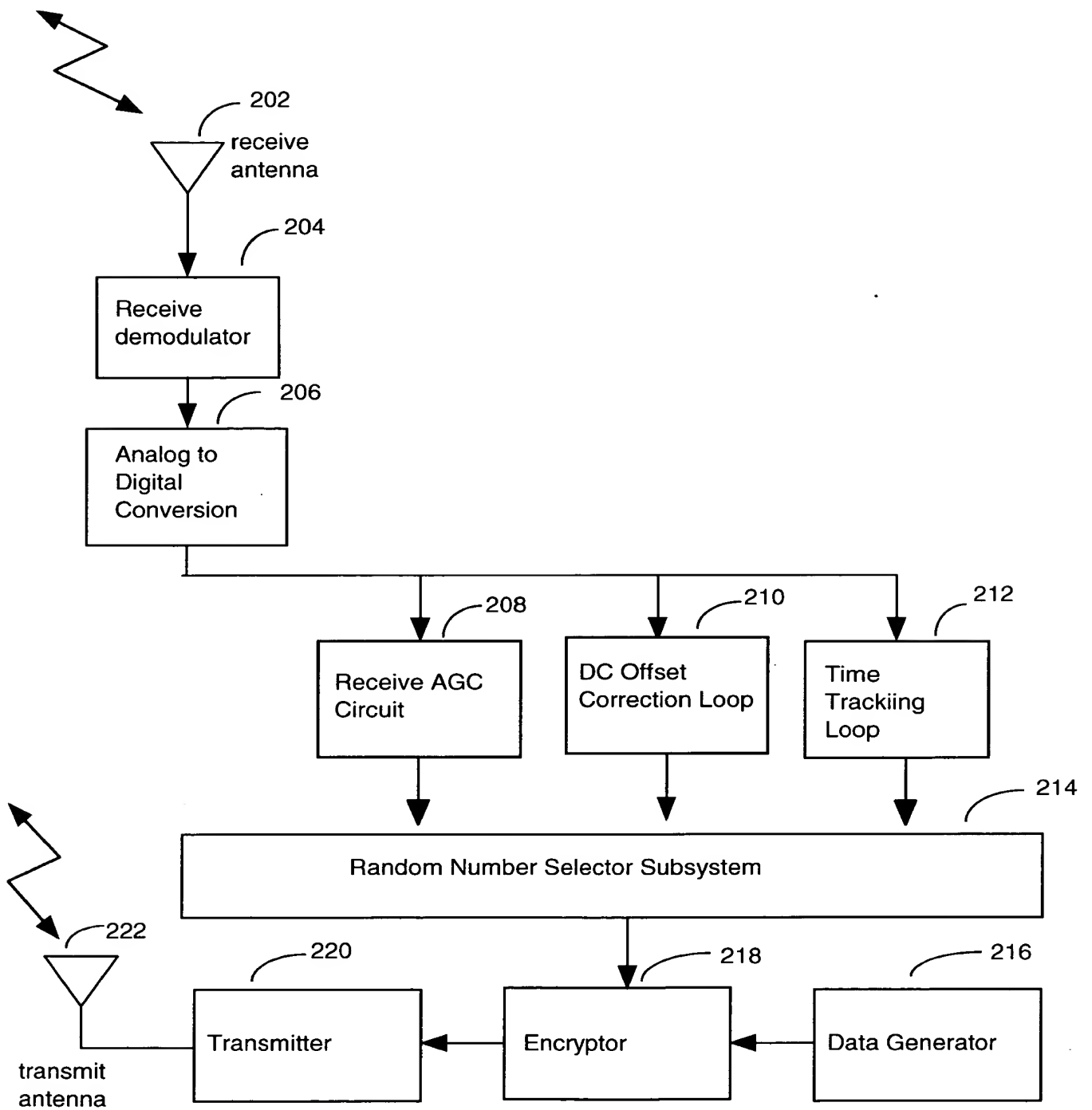


FIGURE 2

FIG. 3 is a block diagram of a receiver system 208, in accordance with an embodiment of the present invention.

208

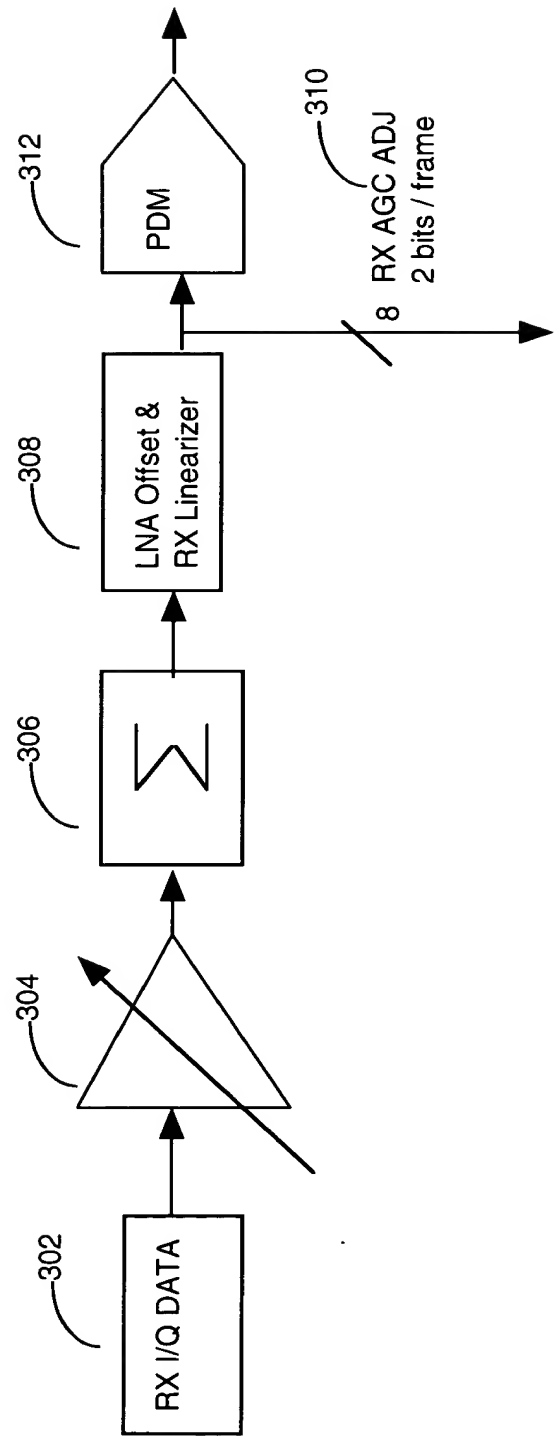


FIGURE 3

210

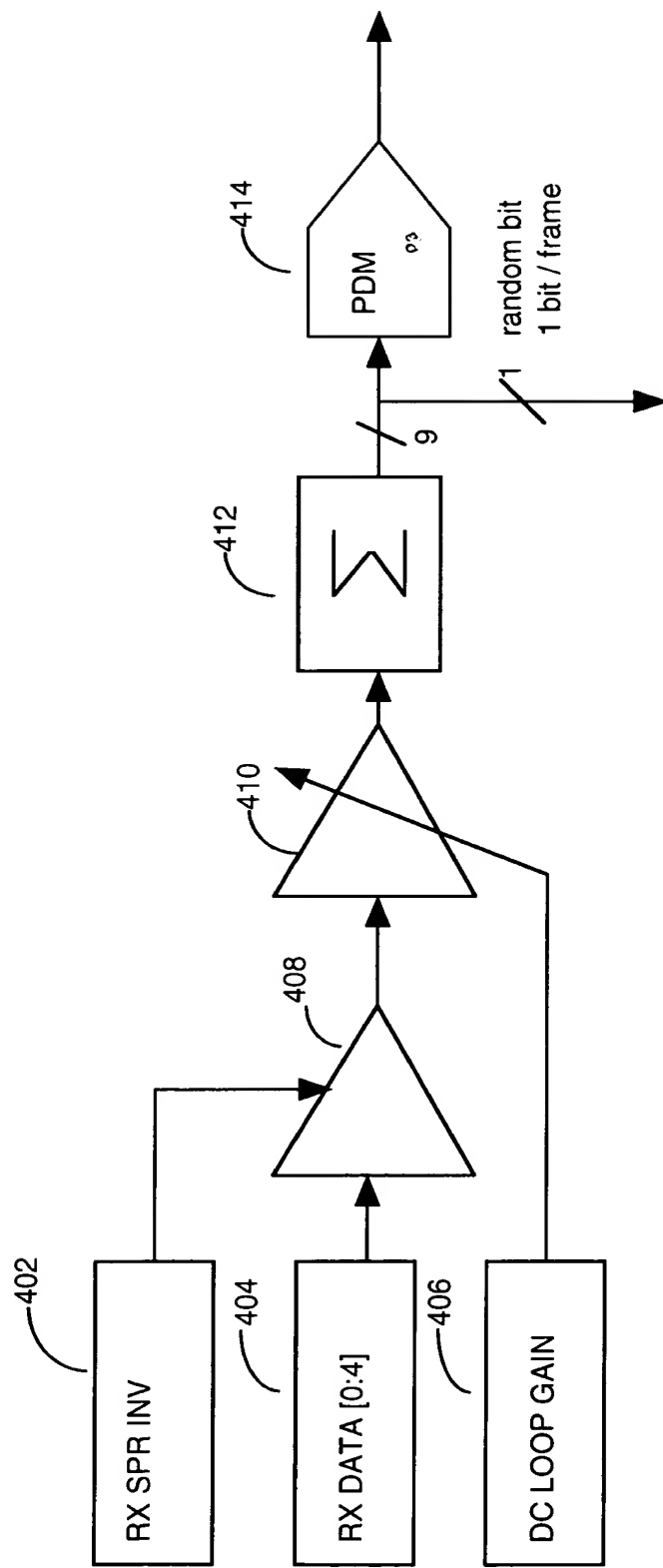


FIGURE 4

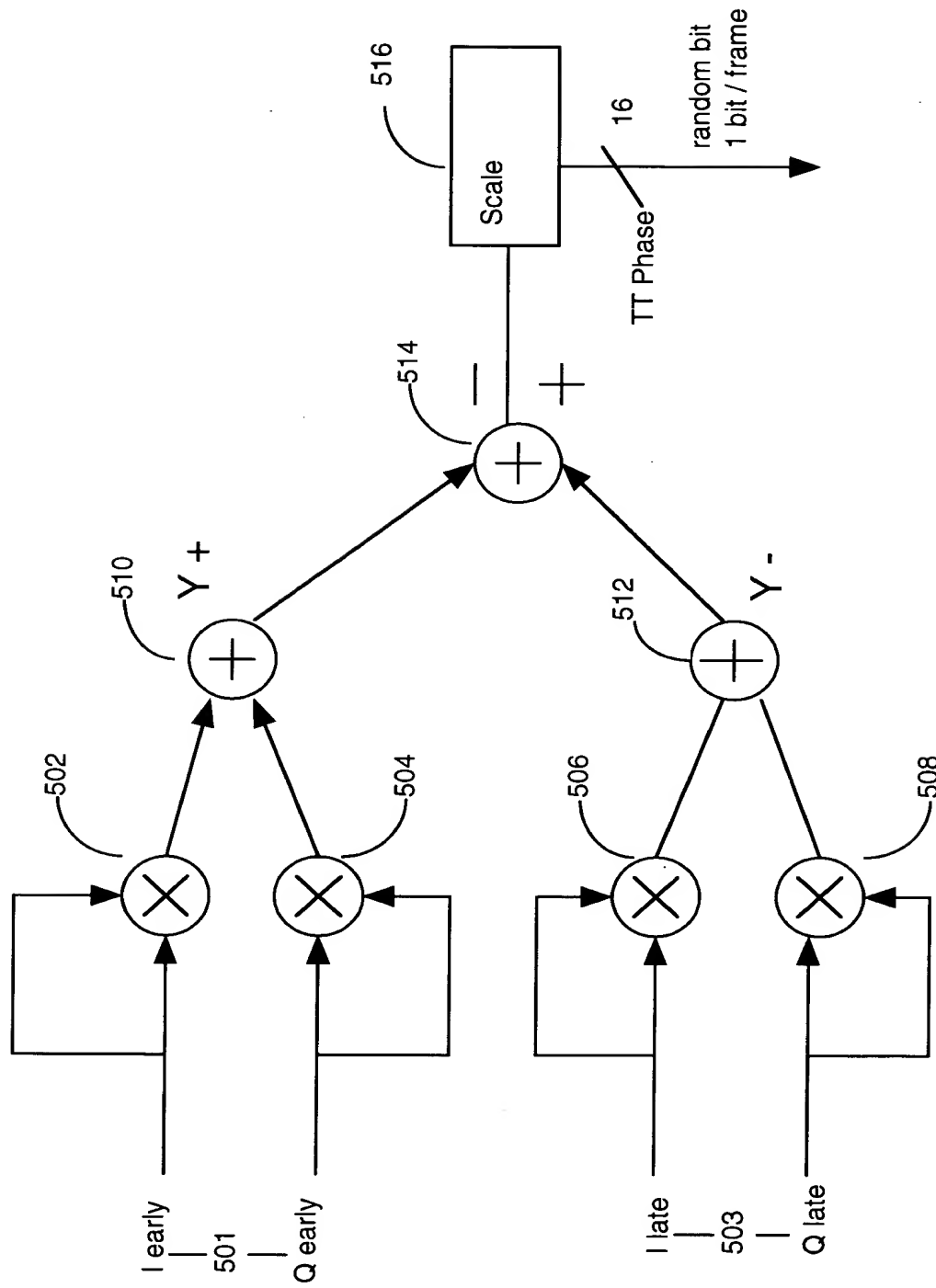


FIGURE 5